

## **Principal Analog IC Design Engineer**

### **EXPERIENCE & QUALIFICATIONS:**

Candidate must have led several projects from specification to IC volume production stages.

Candidate must have in-depth knowledge and prior experience with analog and mixed signal circuit design of blocks such as high performance Low Noise Amplifiers (LNA), filters, data converters (ADC, DAC), reference circuits, IO circuits, etc and should be able to guide junior IC design engineers.

Knowledge of Low power analog design techniques is required.

Candidate should have good knowledge of CMOS devices, device matching and optimizing for low noise designs.

Thorough understanding of simulation models, design rules, verification procedures (DRC / LVS / ERC), and transistor-level simulations is essential.

Candidate should also be familiar with digital design and should be able to drive digital design methodology through a COT design flow.

Candidate should have good understanding of analog layout for performance and should be able to supervise layout designers.

Candidate should be hands-on in testing/debugging ICs in the lab environment and should be able to guide test engineers on ATE.

Familiarity with Labview is desired.

Understanding at system level and using Matlab, Mathcad or equivalent simulators is essential.

Candidate should be able to use system modeling or behavioral modeling to verify system level performance.

Candidate should have experience in IC evaluation/application board design for testing of silicon and should be able to guide board designers, board layout and troubleshoot the boards.

Candidate may also need to interface with customers during the evaluation/testing phase of the ICs.

Clear verbal and written communications skills are required.

Must be a self-starter, comfortable working with small teams in a start-up environment.

**EDUCATION:**

B.S.EE required, with minimum 15 years experience, M.S.EE or Ph.D. preferred.

**OTHER:**

Please direct resumes to [careers@gtronix.com](mailto:careers@gtronix.com)

All submissions should include the job title **Principal Analog IC Design Engineer** in the subject line.

Principals Only. GTronix, Inc. is an equal opportunity employer.