

Senior Analog IC Design Engineer

EXPERIENCE & QUALIFICATIONS:

Minimum 10 years of experience in analog Integrated Circuit (IC) Design is required.

In-depth knowledge and prior experience with analog and mixed signal circuit design of blocks such as Low Noise Amplifiers (LNA), filters, data converters (ADC, DAC), reference circuits, IO circuits, etc is essential.

Knowledge of Low power analog design techniques is required. Candidate should have good knowledge of CMOS devices, device matching and optimizing for low noise designs.

Thorough understanding of simulation models, design rules, verification procedures (DRC / LVS / ERC), and transistor-level simulations is essential. Familiarity with Cadence tools is required.

Candidate should have good understanding of analog layout for performance and should be able to guide layout designers.

Candidate should be hands-on in testing/debugging ICs in the lab environment and should be able to guide test engineers on ATE.

Understanding at system level and using Matlab or equivalent simulators is highly desired.

Clear verbal and written communications skills are important.

Must be a self-starter, comfortable working with small teams in a start-up environment.

EDUCATION:

B.S.EE required, with minimum 10 years experience, M.S.EE or Ph.D. preferred.

OTHER:

Please direct resumes to careers@gtronix.com

All submissions should include the job title **Senior Analog IC Design Engineer** in the subject line.

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