

Senior Digital IC Design Engineer

EXPERIENCE & QUALIFICATIONS:

Candidate should have delivered multiple designs from specification to silicon validation.

Candidate should have experience in all phases of digital design viz. micro-architecture, RTL design, RTL verification, DFT, Synthesis, Timing Analysis, Test vector generation, P & R, Physical Verification and silicon bring-up.

Candidate should be experienced in necessary CAD tools for all the phases of digital design from front-end to back-end.

Knowledge of scripting for automation is highly desired.

Candidate should have familiarity with analog & mixed signal blocks & systems since the digital design will be part of a larger analog/mixed signal chip.

Clear verbal and written communications skills are important

Must be a self-starter, comfortable working with small teams in a start-up environment.

EDUCATION:

B.S.EE required, with minimum 10 years experience

OTHER:

Please direct resumes to careers@gtronix.com

All submissions should include the job title **Senior Digital IC Design Engineer Job Description** in the subject line.

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